FIG. 1

DELAY PROFILE

CORRECT PATH POSITIONS

FINGERS ASSIGNED TO PATHS

PATH 1

PATH 2

PATH 3

PATH 4

NOISE (INTERFERENCE) LEVEL

FIG. 2

POWER

PATH 1

PATH 2

PATH 3

PATH 4

FIG. 3

102 RADIO RECEIVING SECTION

103a DESPREADING

104a AVERAGING

106a RSCP CALCULATION

108 COMBINING

109 COMBINING

110 ISCP CALCULATION

111 AVERAGING

112 SIR CALCULATION

MEASURING SIR

FIG. 4A, FIG. 4B, FIG. 5B

PATH 1

PATH 2

PATH 3

PATH 4

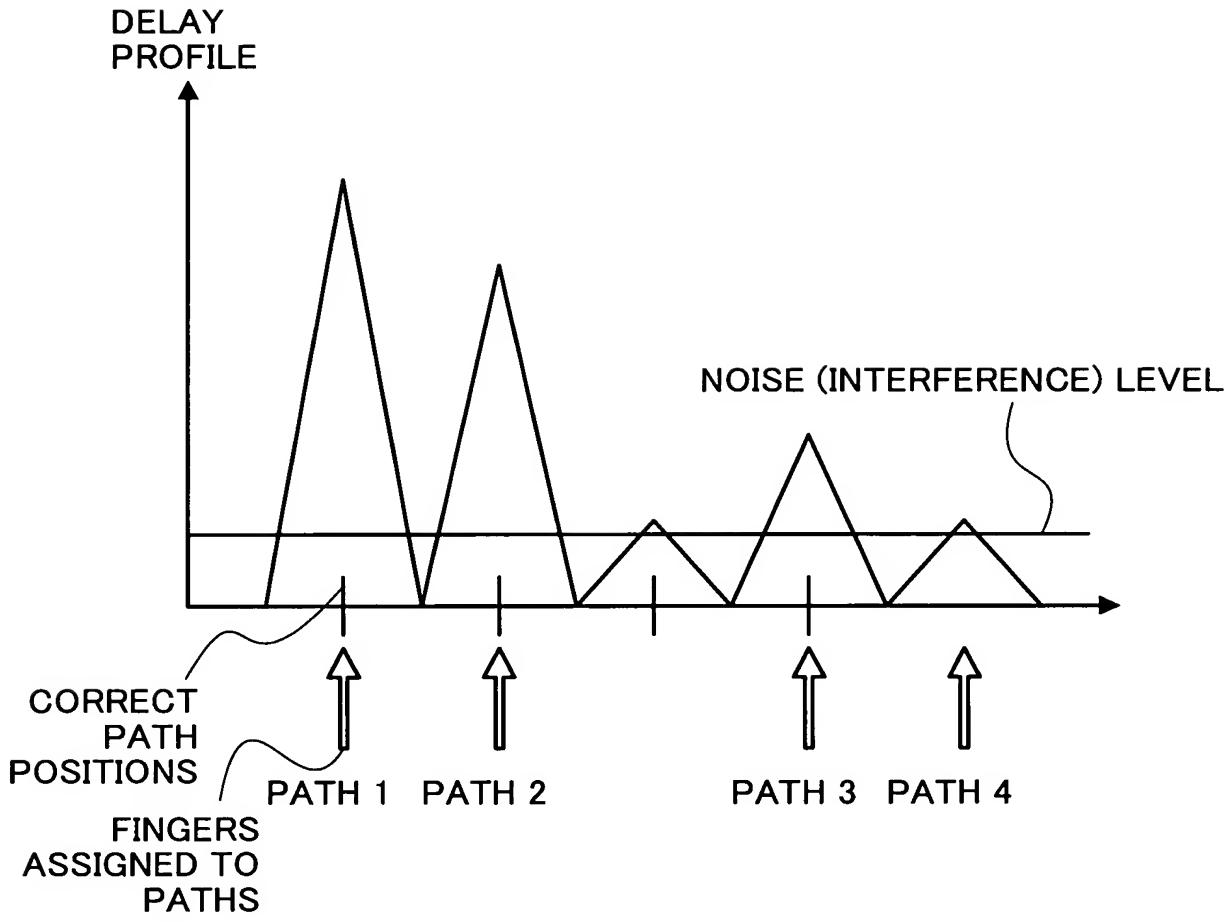
DELAY
PROFILE
NOISE (INTERFERENCE) LEVEL
CORRECT
PATH
POSITIONS
FINGERS
ASSIGNED TO
PATHS
PATH 1
PATH 2
PATH 3
PATH 4

FIG.1

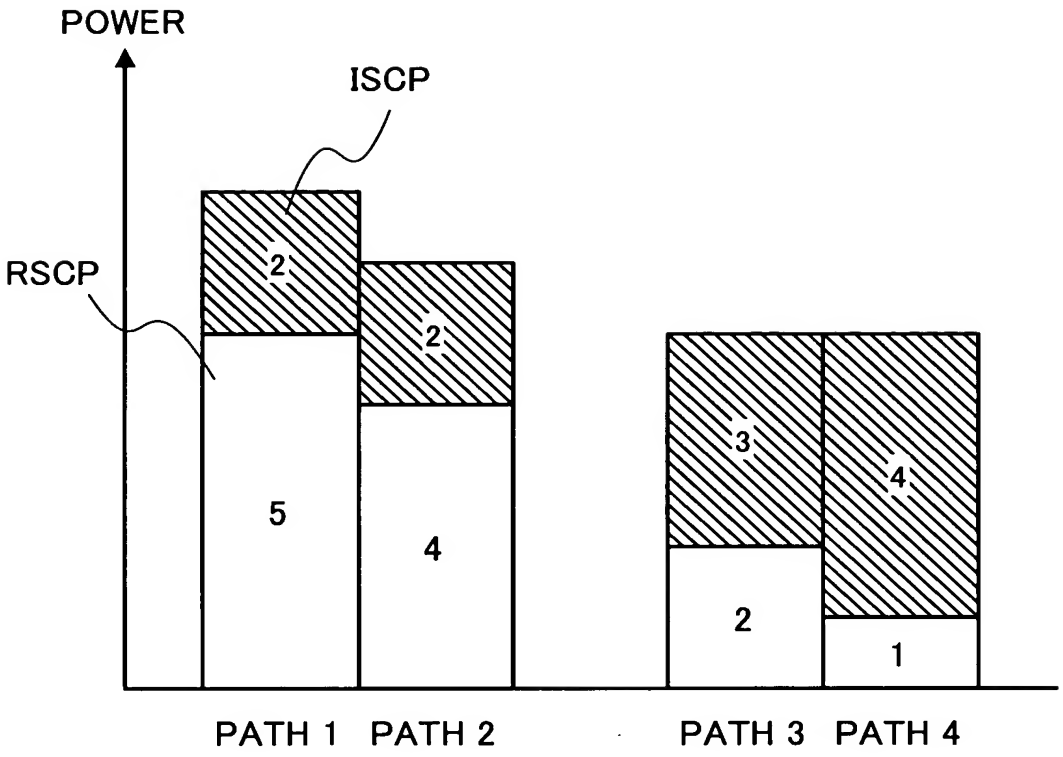

FIG.2

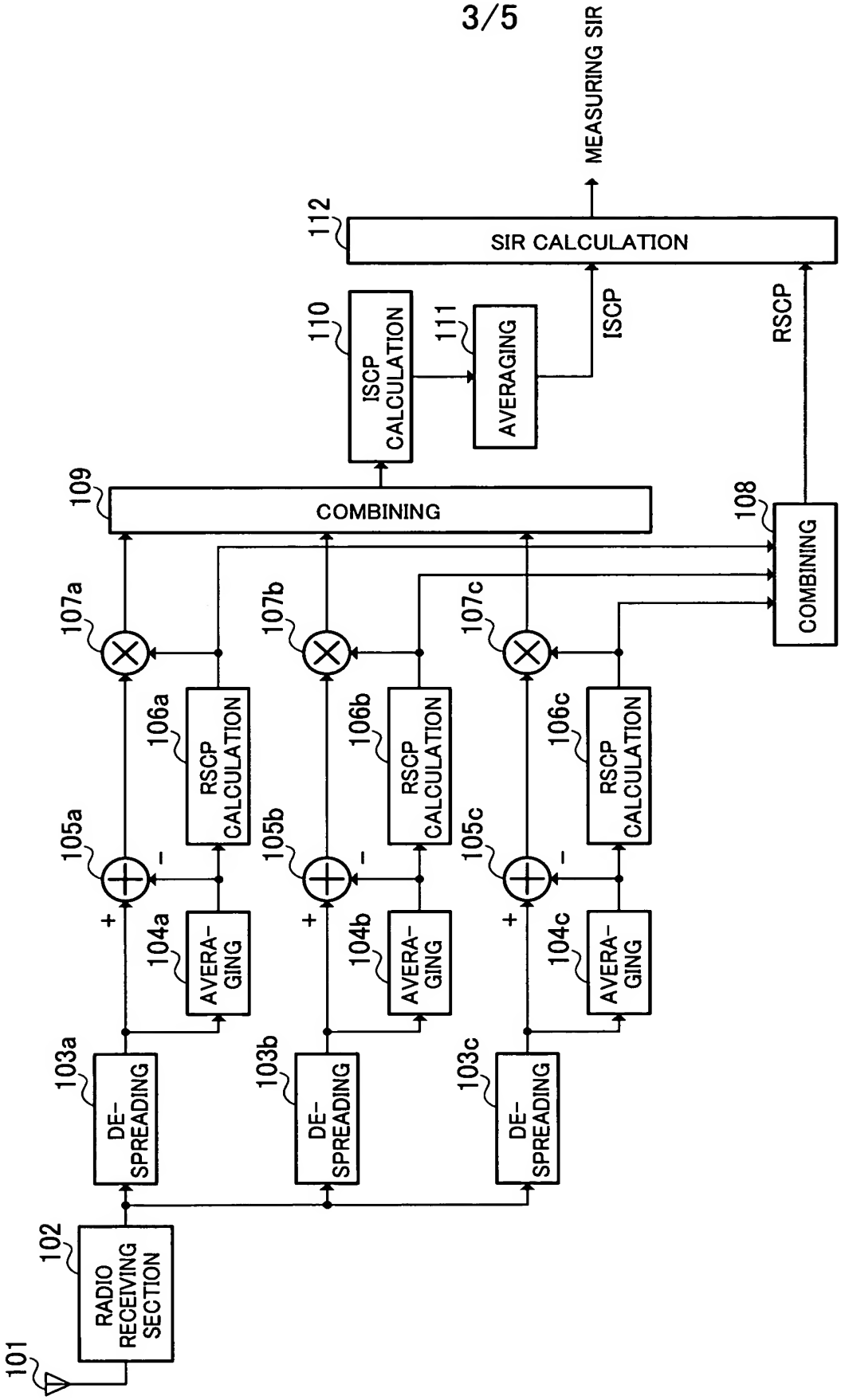
MEASURING SIR
112
SIR CALCULATION
110
ISCP CALCULATION
111
AVERAGING
ISCP
RSCP
109
COMBINING
108
COMBINING
107a
107b
107c
106a
RSCP CALCULATION
106b
RSCP CALCULATION
106c
RSCP CALCULATION
105a
105b
105c
104a
AVERA-GING
104b
AVERA-GING
104c
AVERA-GING
103a
DE-SPREADING
103b
DE-SPREADING
103c
DE-SPREADING
102
RADIO RECEIVING SECTION
101

FIG.3

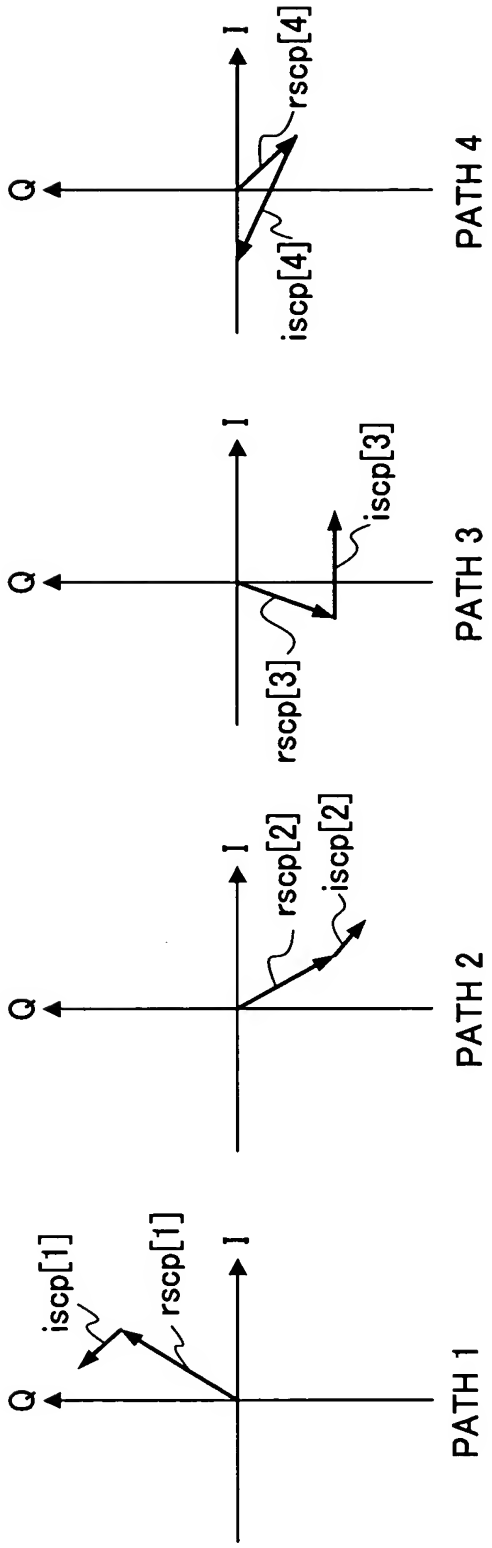
Q
I
iscp[1]
rscp[1]
PATH 1
Q
I
rscp[2]
iscp[2]
PATH 2
Q
I
iscp[3]
rscp[3]
PATH 3
Q
I
rscp[4]
iscp[4]
PATH 4

FIG.4A

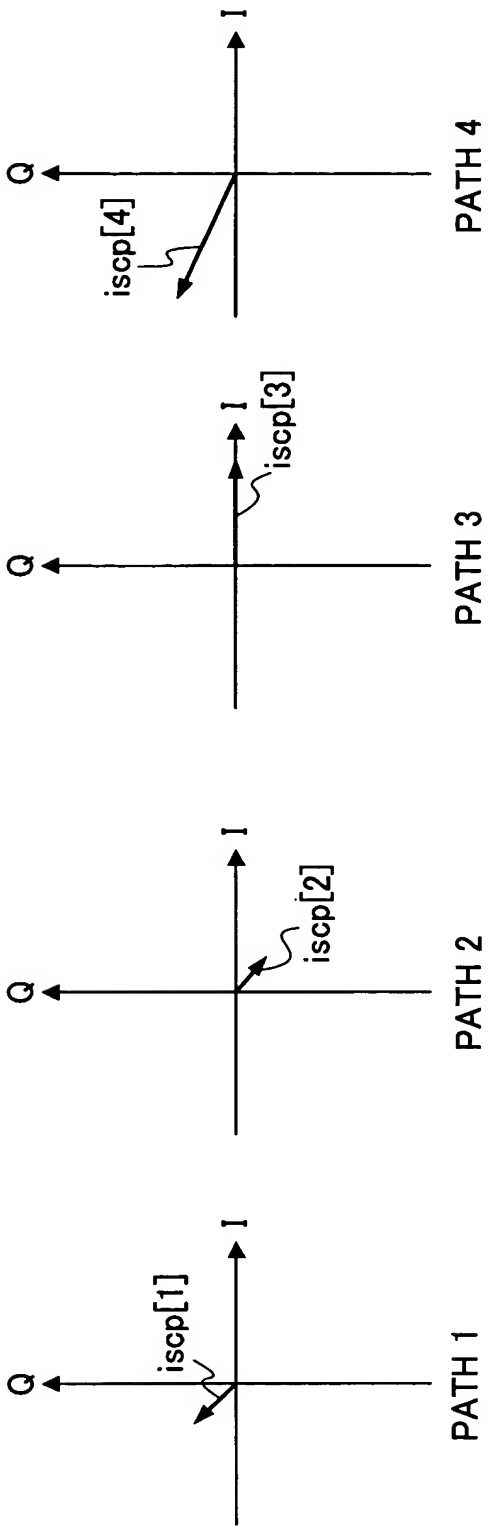
Q
I
iscp[1]
PATH 1
Q
I
iscp[2]
PATH 2
Q
I
iscp[3]
PATH 3
Q
I
iscp[4]
PATH 4

FIG.4B

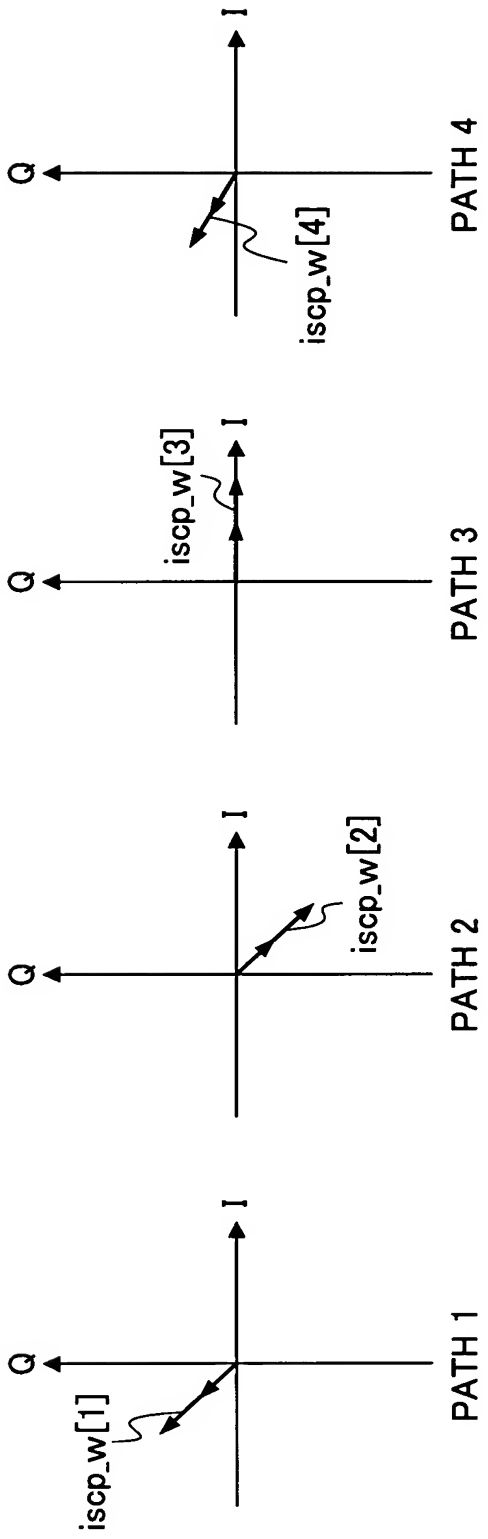
I
Q
iscp_w[1]
PATH 1
I
Q
iscp_w[2]
PATH 2
I
Q
iscp_w[3]
PATH 3
I
Q
iscp_w[4]
PATH 4

FIG.5A

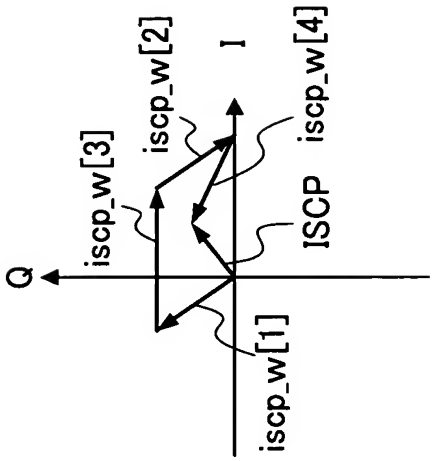
Q
I
iscp_w[3]
iscp_w[2]
iscp_w[4]
ISCP
iscp_w[1]

FIG.5B